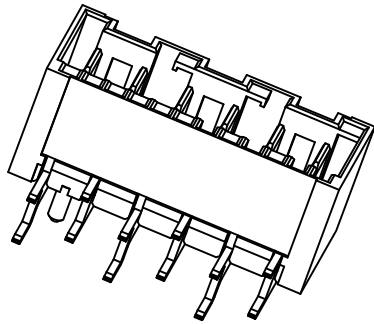
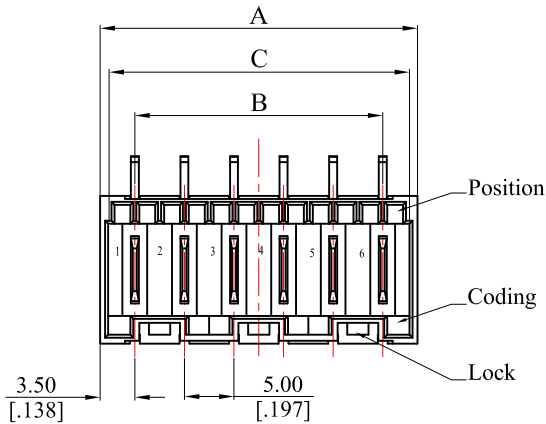


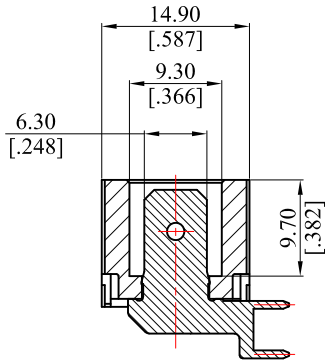
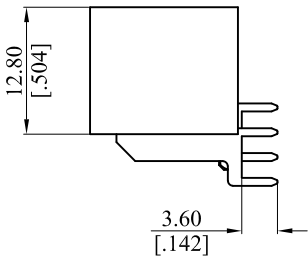
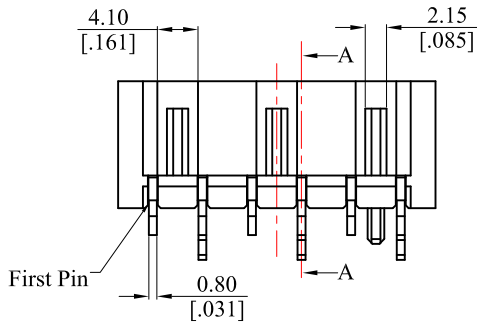
REV.	ECO. NO.	DISCRIPTION	DATE	DRAW BY
L	ES250922	1.Add package instruction 2.Change PCB hole diameter from 1.05+0.10/-0 to 1.3+0.1	2025.12.09	Merry



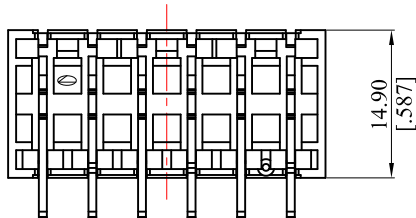
Reverse type

Notes:

1. Material:
Insulator: PA66+30% G.F.,UL94V-0,Natural
Terminal: Copper alloy
2. Plating:
5011TCBTH**:
Tin Plated 120μ" min. over Ni 30μ" min.
5011TCBSH** :
Silver Plated 80μ" min. over Ni 30μ" min.
3. Specification:
Voltage rating:380V
Current rating:16A Max.
Temperature rating:-40°C~+120°C
4. UL&VDE Certification:
UL Certification No. E82349,applicable to circuits 2 to 12
VDE Certification No. 40017373,applicable to circuits 2 to 12



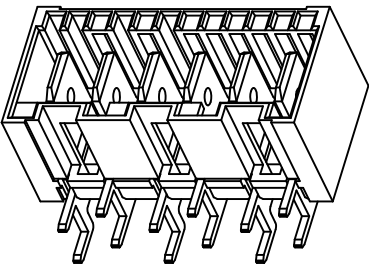
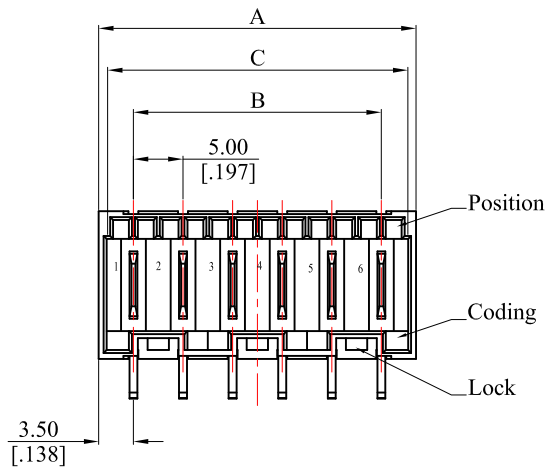
SECTION A-A



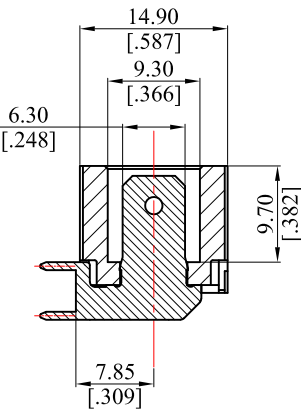
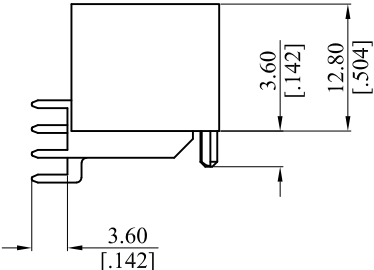
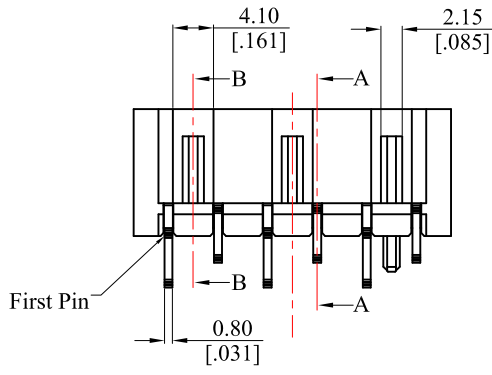
12	62.0(2.441)	55.0(2.165)	60.2(2.370)
11	57.0(2.244)	50.0(1.969)	55.2(2.173)
10	52.0(2.047)	45.0(1.712)	50.2(1.975)
9	47.0(1.850)	40.0(1.575)	45.2(1.780)
8	42.0(1.654)	35.0(1.378)	40.2(1.583)
7	37.0(1.457)	30.0(1.181)	35.2(1.386)
6	32.0(1.260)	25.0(.984)	30.2(1.189)
5	27.0(1.063)	20.0(.787)	25.2(.992)
4	22.0(.886)	15.0(.591)	20.2(.795)
3	17.0(.669)	10.0(.394)	15.2(.598)
2	12.0(.472)	5.0(.197)	10.2(.402)
No. of circuits	DIM.A	DIM.B	DIM.C

TOLERANCE UNLESS OTHERWISE SPECIFIED			LEOCO CORPORATION			
mm	mm	inch	http://www.leoco.com			
[inch]	mm	inch	APPROVED BY:	DATE:	TITLE:	
1 PLACE	± 0.30	± .012	Chard	2025.12.15		
2 PLACES	± 0.20	± .008	CHECKED BY:	DATE:	Rast5 Male Header	
3 PLACES	± 0.10	± .004	Topmoon	2025.12.12		
4 PLACES	± --	± --	DRAWN BY:	DATE:	PART NO.: 5011P*****	
ANGULAR ±1°			Merry	2025.12.09		
SIZE:	SCALE:		DRAWING NO.:	REV.:	SHEET:	
A3	1:1		501103S	L	2 of 4	

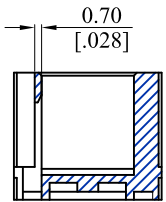
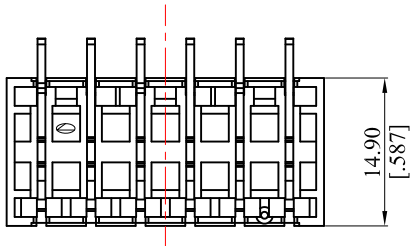
REV.	ECO. NO.	DISCRIPTION	DATE	DRAW BY
L	ES250922	1.Add package instruction 2.Change PCB hole diameter from 1.05+0.10/-0 to 1.3±0.1	2025.12.09	Merry



Normal type



SECTION A-A



SECTION B-B

Notes:

1. Material:
Insulator: PA66+30% G.F.,UL94V-0,Natural
Terminal: Copper alloy
2. Plating:
5011TCBTH**:
Tin Plated 120μ" min. over Ni 30μ" min.
5011TCBSH** :
Silver Plated 80μ" min. over Ni 30μ" min.
3. Specification:
Voltage rating:380V
Current rating:16A Max.
Temperature rating:-40℃~+120℃
4. UL&VDE Certification:
UL Certification No. E82349,applicable to circuits 2 to 12
VDE Certification No. 40017373,applicable to circuits 2 to 12

12	62.0(2.441)	55.0(2.165)	60.2(2.370)
11	57.0(2.244)	50.0(1.969)	55.2(2.173)
10	52.0(2.047)	45.0(1.712)	50.2(1.975)
9	47.0(1.850)	40.0(1.575)	45.2(1.780)
8	42.0(1.654)	35.0(1.378)	40.2(1.583)
7	37.0(1.457)	30.0(1.181)	35.2(1.386)
6	32.0(1.260)	25.0(.984)	30.2(1.189)
5	27.0(1.063)	20.0(.787)	25.2(.992)
4	22.0(.886)	15.0(.591)	20.2(.795)
3	17.0(.669)	10.0(.394)	15.2(.598)
2	12.0(.472)	5.0(.197)	10.2(.402)
No. of circuits	DIM.A	DIM.B	DIM.C

TOLERANCE UNLESS OTHERWISE SPECIFIED			LEOCO CORPORATION			
mm	mm	inch	http://www.leoco.com			
[inch]						
1 PLACE	± 0.30	± .012	APPROVED BY:	DATE:	TITLE: Rast5 Male Header	
2 PLACES	± 0.20	± .008	Chard	2025.12.15		
3 PLACES	± 0.10	± .004	CHECKED BY:	DATE:	PART NO.: 5011P*****	
4 PLACES	± --	± --	Topmoon	2025.12.12		
			DRAWN BY:	DATE:	DRAWING NO.: 501103S	
			Merry	2025.12.09		
ANGULAR ±1°			SIZE:	SCALE:	REV.:	SHEET:
			A3	1:1	L	1 of 4

Ordering Information:

5011P ** H **

1 2 3 4

1. No. of circuits

2. Type:

H:Normal type R:Reverse type

3. Terminal(The drawings are as below):

T=Assemble terminal 5011TCBTHC*

L=Assemble terminal 5011TCBTHD*

J=Assemble terminal 5011TCBTHC*

With special layout

G=Assemble terminal 5011TCBTHD*

With special layout

4. Coding and Lock option refer to the file:

The file no. is 501105P(5011KEY0000).

C=Assemble terminal 5011TCBSHC*

D=Assemble terminal 5011TCBSDH*

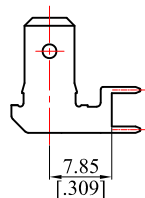
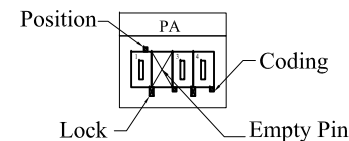
K=Assemble terminal 5011TCBSHC*

With special layout

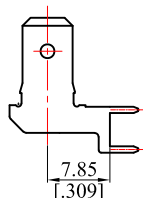
Z=Assemble terminal 5011TCBSDH*

With special layout

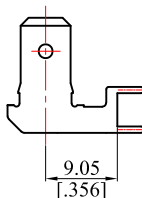
For example:



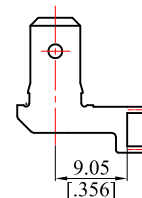
Part no.:5011TCBTHC1



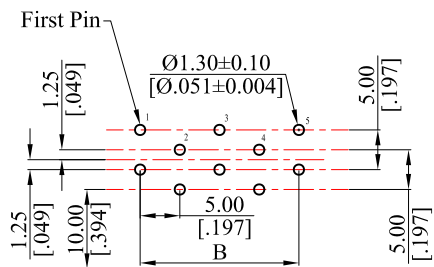
Part no.:5011TCBTHC2



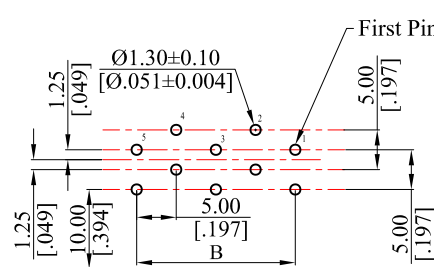
Part no.:5011TCBTHD1



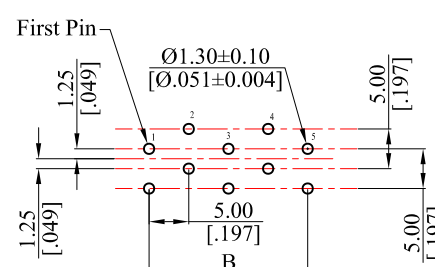
Part no.:5011TCBTHD2



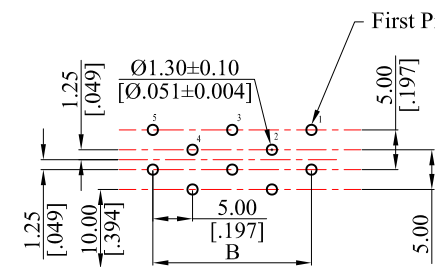
PCB layout for normal (5011P**HT**/5011P**HL**) PCB edge



PCB layout for reverse (5011P**RT**/5011P**RL**) PCB edge



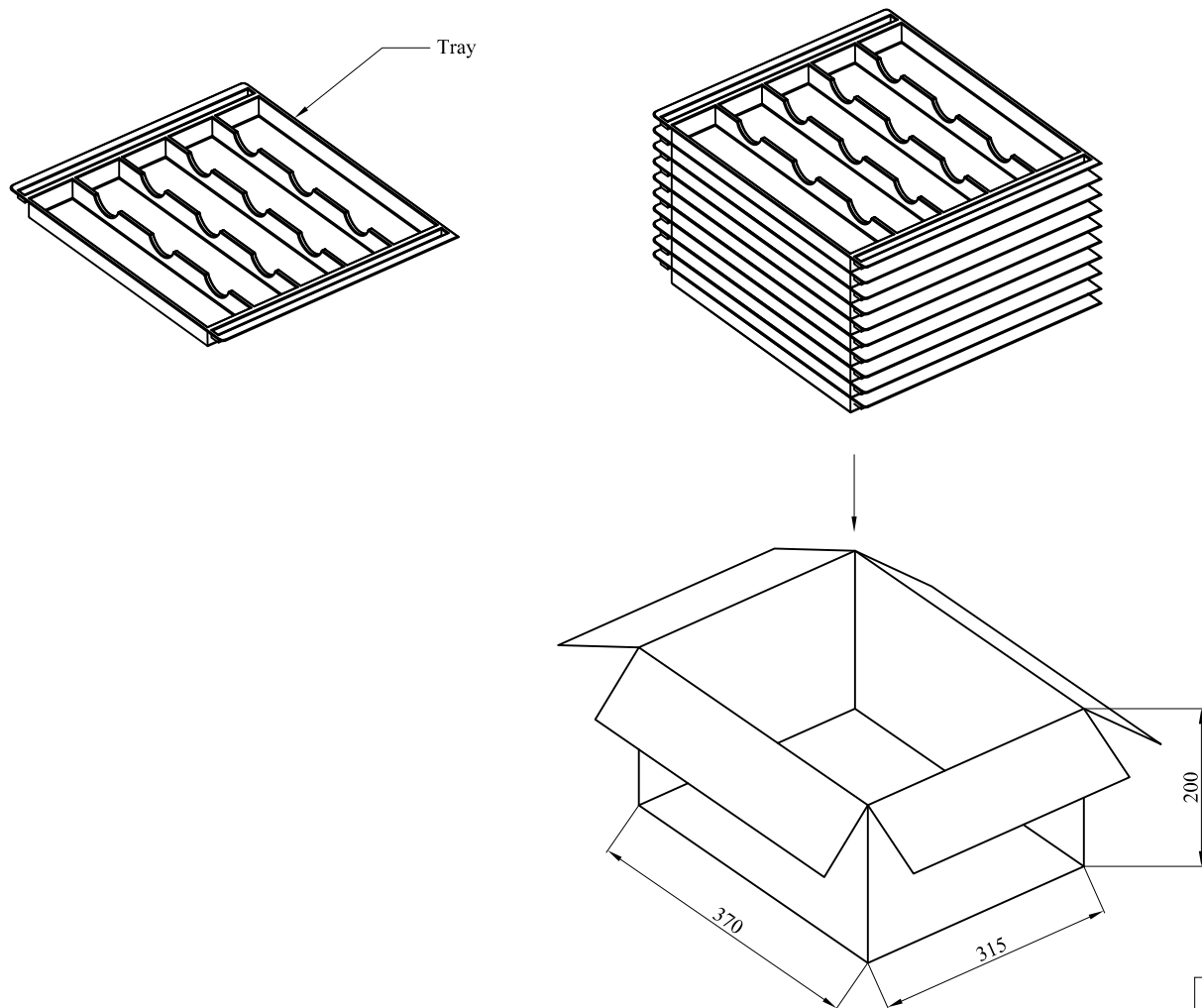
Special PCB layout for normal (5011P**HJ**/5011P**HG**) PCB edge



Special PCB layout for reverse (5011P**RJ**/5011P**RG**) PCB edge

TOLERANCE UNLESS OTHERWISE SPECIFIED			LEOCO CORPORATION			
mm	mm	inch	http://www.leoco.com			
[inch]	mm	inch	APPROVED BY:	DATE:	TITLE:	
1 PLACE	± 0.30	± .012	Chard	2025.12.15	Rast5 Male Header	
2 PLACES	± 0.20	± .008	CHECKED BY:	DATE:		
3 PLACES	± 0.10	± .004	Topmoon	2025.12.12	PART NO.: 5011P*****	
4 PLACES	± --	± --	Merry	2025.12.09		
ANGULAR ±1°			SIZE: A3	SCALE: 1:1	DRAWING NO.: 501103S	REV.: L SHEET: 3 of 4

Packaging:Tray



REV.	ECO. NO.	DISCRIPTION	DATE	DRAW BY
L	ES250922	1.Add package instruction 2.Change PCB hole diameter from 1.05+0.10/-0 to 1.3+0.1	2025.12.09	Merry

Part No.	Packing Quantities	
	Pcs/Tray	Pcs/Carton
5011P02*T(C/J/K)**	360	3240
5011P03*T(C/J/K)**	255	2295
5011P04*T(C/J/K)**	195	1755
5011P05*T(C/J/K)**	150	1350
5011P06*T(C/J/K)**	135	1215
5011P07*T(C/J/K)**	105	945
5011P08*T(C/J/K)**	90	810
5011P09*T(C/J/K)**	90	810
5011P10*T(C/J/K)**	75	675
5011P11*T(C/J/K)**	50	450
5011P12*T(C/J/K)**	40	360
5011P02*L(D/G/Z)**	300	2700
5011P03*L(D/G/Z)**	225	2025
5011P04*L(D/G/Z)**	150	1350
5011P05*L(D/G/Z)**	125	1125
5011P06*L(D/G/Z)**	120	1080
5011P07*L(D/G/Z)**	75	675
5011P08*L(D/G/Z)**	75	675
5011P09*L(D/G/Z)**	75	675
5011P10*L(D/G/Z)**	75	675
5011P11*L(D/G/Z)**	50	450
5011P12*L(D/G/Z)**	40	360

TOLERANCE UNLESS OTHERWISE SPECIFIED			LEOCO CORPORATION http://www.leoco.com						
mm			APPROVED BY:		DATE:		TITLE:		
[inch]	mm	inch	Chard		2025.12.15				
1 PLACE	± 0.30	± .012	CHECKED BY:		DATE:		Rast5 Male Header		
2 PLACES	± 0.20	± .008	Topmoon		2025.12.12				
3 PLACES	± 0.10	± .004	DRAWN BY:		DATE:		PART NO.: 5011P*****		
4 PLACES	± —	± —	Merry		2025.12.09				
ANGULAR ±1°			SIZE:	SCALE:			DRAWING NO.:	REV.:	SHEET:
			A3	1:1			501103S	L	4 of 4