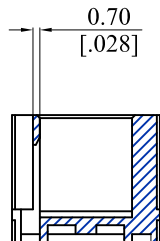
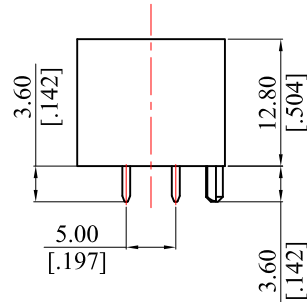
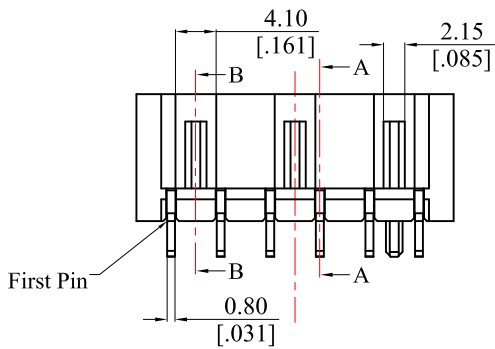
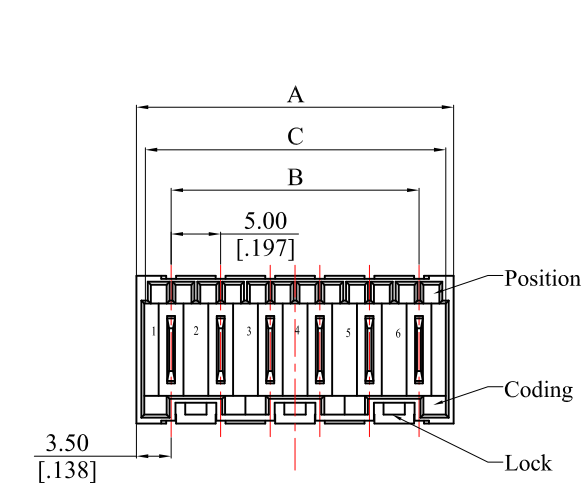
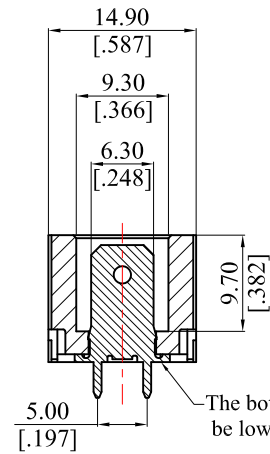
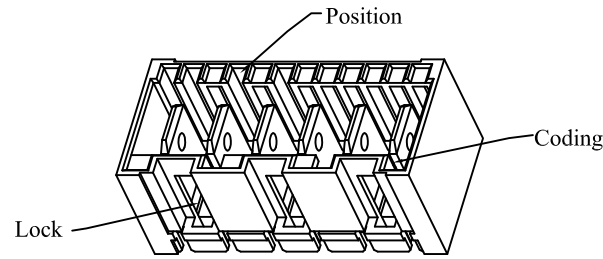


REV.	ECO. NO.	DISCRIPTION	DATE	DRAW BY
N	ES250922	1.Add packaging instruction 2.Change PCB hole diameter from 1.05+0.10/-0 to 1.3+0.1	2025.12.09	Merry



SECTION B-B



SECTION A-A

Notes:

- Material:
Insulator: PA66+30% G.F.,UL94V-0,Natural
Terminal: Copper alloy
- Plating:
5011TCBTVC* :
Tin Plated 120μ" min. over Ni 30μ" min.
5011TCBSVC* :
Silver Plated 80μ" min. over Ni 30μ" min.
- Specification:
Voltage rating:380V
Current rating:16A Max.
Temperature rating:-40°C~+120°C
- UL&VDE Certification:
UL Certification No. E82349,applicable to circuits 2 to 12
VDE Certification No. 40017373,applicable to circuits 2 to 12

	12	62.0(2.441)	55.0(2.165)	60.2(2.370)
	11	57.0(2.244)	50.0(1.969)	55.2(2.173)
	10	52.0(2.047)	45.0(1.712)	50.2(1.975)
	9	47.0(1.850)	40.0(1.575)	45.2(1.780)
	8	42.0(1.654)	35.0(1.378)	40.2(1.583)
	7	37.0(1.457)	30.0(1.181)	35.2(1.386)
	6	32.0(1.260)	25.0(.984)	30.2(1.189)
	5	27.0(1.063)	20.0(.787)	25.2(.992)
	4	22.0(.886)	15.0(.591)	20.2(.795)
	3	17.0(.669)	10.0(.394)	15.2(.598)
	2	12.0(.472)	5.0(.197)	10.2(.402)
No. of circuits	DIM.A	DIM.B	DIM.C	

TOLERANCE UNLESS OTHERWISE SPECIFIED			LEOCO CORPORATION			
mm			http://www.leoco.com			
[inch]	mm	inch	APPROVED BY:	DATE:	TITLE:	
1 PLACE	± 0.30	± .012	Chard	2025.12.15	Rast5 Male Header	
2 PLACES	± 0.20	± .008	CHECKED BY:	DATE:		
3 PLACES	± 0.10	± .004	Topmoon	2025.12.12	PART NO.: 5011P**V***	
4 PLACES	± --	± --	Merry	2025.12.09		
ANGULAR ±1°			SIZE: A3	SCALE: 1:1	DRAWING NO.: 501102S	REV.: N SHEET: 1 of 3

REV.	ECO. NO.	DISCRIPTION	DATE	DRAW BY
N	ES250922	1.Add packaging instruction 2.Change PCB hole diameter from 1.05+0.10/-0 to 1.3±0.1	2025.12.09	Merry

Ordering Information:

5011P ** V * **
1 2 3 4

1. No. of circuits

2. Type: V type

3. Terminal(The drawings are as below):

T=Assemble terminal 5011TCBTVCS

P=Assemble terminal 5011TCBSVC0

S=Assemble terminal 5011TCBTVCS

Q=Assemble terminal 5011TCBSVCS

X=Assemble terminal 5011TCBTVCS

Y=Assemble terminal 5011TCBSVCX

V=Assemble terminal 5011TCBTVCS

W=Assemble terminal 5011TCBSVCX

With special layuot

With special layuot

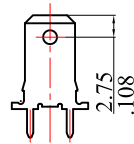
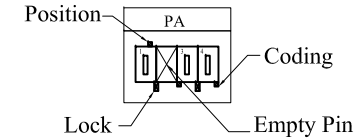
M=Assemble terminal 5011TCBTVCM

N=Assemble terminal 5011TCBSVCM

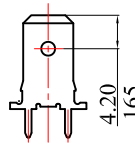
4. Coding and Lock option refer to the file:

The file no. is 501105P(5011KEY0000).

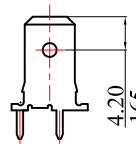
For example:



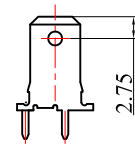
Part no.:5011TCB*VCS



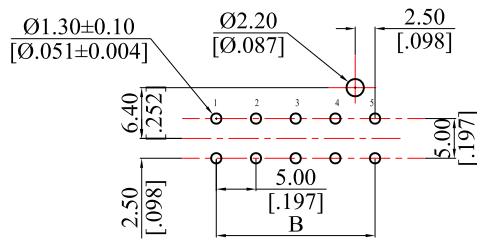
Part no.:5011TCB*VC0



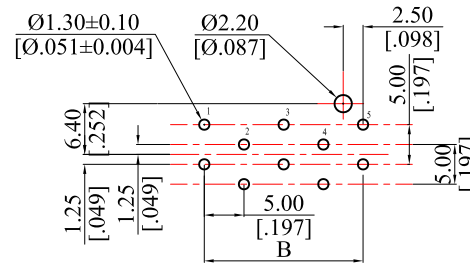
Part no.:5011TCB*VCX



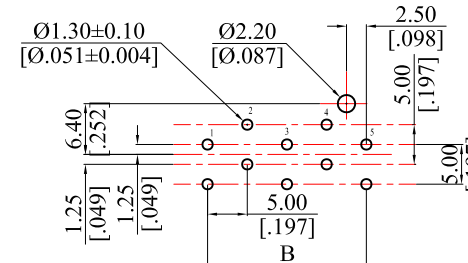
Part no.:5011TCB*VCM



PCB layout for 5011P**VT(S)**



PCB layout for 5011P**VX(M)**

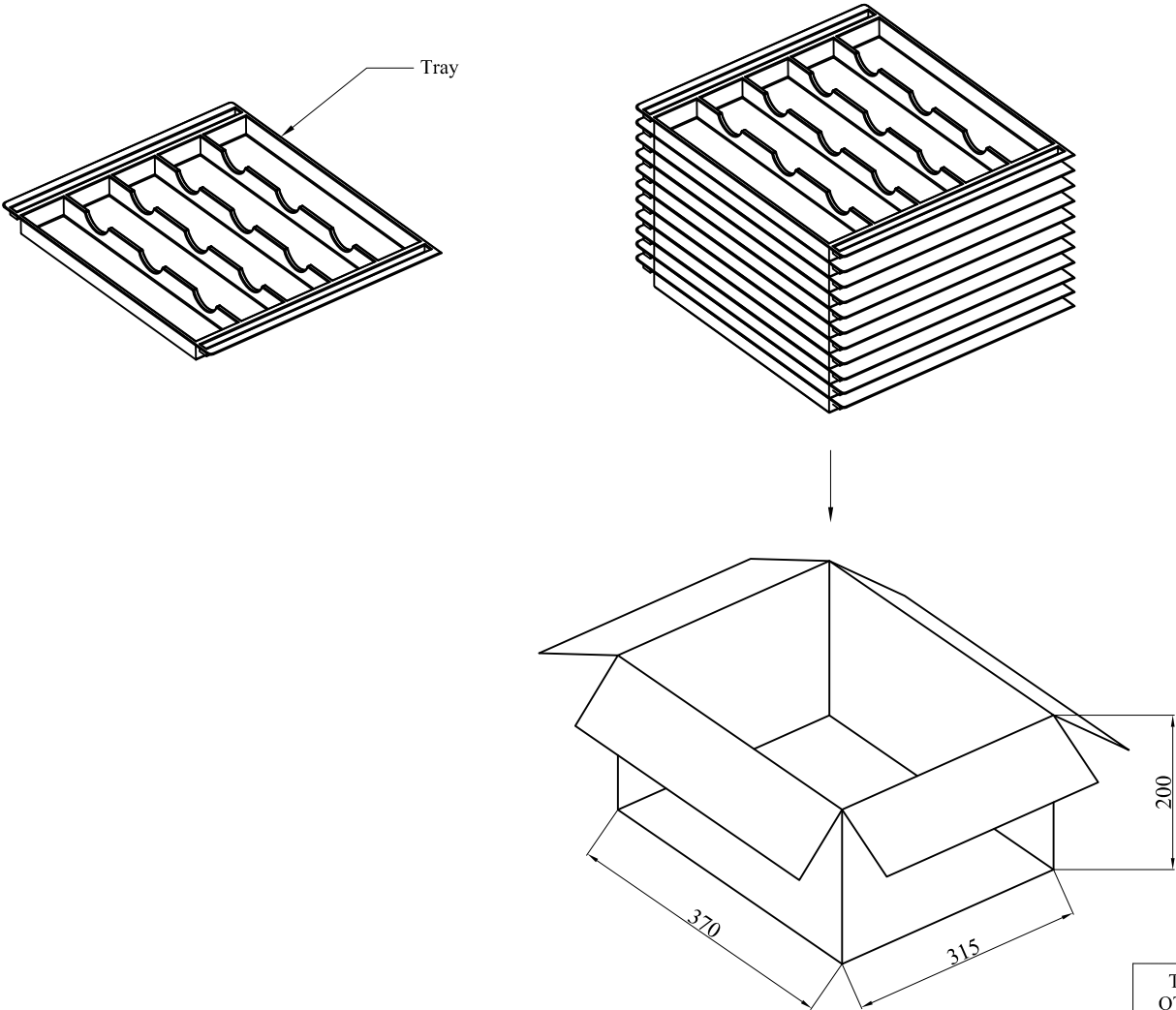


Special PCB layout for 5011P**VV(W)**

TOLERANCE UNLESS OTHERWISE SPECIFIED			LEOCO CORPORATION			
mm			http://www.leoco.com			
[inch]	mm	inch	APPROVED BY:	DATE:	TITLE:	
1 PLACE	± 0.30	± .012	Chard	2025.12.15		
2 PLACES	± 0.20	± .008	CHECKED BY:	DATE:	Rast5 Male Header	
3 PLACES	± 0.10	± .004	Topmoon	2025.12.12		
4 PLACES	± --	± --	DRAWN BY:	DATE:	PART NO.: 5011P**V***	
ANGULAR ±1°			Merry	2025.12.09		
SIZE:	A3	SCALE:	~		DRAWING NO.: 501102S	REV.: N
					SHEET: 2 of 3	

REV.	ECO. NO.	DISCRIPTION	DATE	DRAW BY
N	ES250922	1.Add packaging instruction 2.Change PCB hole diameter from 1.05+0.10/-0 to 1.3±0.1	2025.12.09	Merry

Packaging:Tray



Part no.	Packing Quantities	
	Pcs/Tray	Pcs/Carton
5011P02V***	360	3600
5011P03V***	255	2550
5011P04V***	195	1950
5011P05V***	150	1500
5011P06V***	135	1350
5011P07V***	105	1050
5011P08V***	90	900
5011P09V***	90	900
5011P10V***	75	750
5011P11V***	75	750
5011P12V***	60	600

TOLERANCE UNLESS OTHERWISE SPECIFIED			 LEOCO CORPORATION <i>http://www.leoco.com</i>					
mm			APPROVED BY: DATE:		TITLE:			
[inch]	mm	inch	Chard	2025.12.15				
1 PLACE	± 0.30	± .012	CHECKED BY: DATE:		Rast5 Male Header			
2 PLACES	± 0.20	± .008	Topmoon 2025.12.12					
3 PLACES	± 0.10	± .004	DRAWN BY: DATE:		PART NO.: 5011P**V***			
4 PLACES	± —	± —	Merry 2025.12.09					
ANGULAR ±1°			SIZE: A3	SCALE: ~		DRAWING NO.: 501102S	REV.: N	SHEET: 3 of 3